

Amended Claims With Mark-ups to Show Changes Made

14. (Twice Amended) A plasma display panel, comprising:

a first substrate;

a plurality of first substrate electrode pairs formed on the first substrate;

a second substrate;

second substrate electrodes formed on the second substrate to cross the first substrate electrode pairs;

a first dielectric layer formed on the second substrate and the second substrate electrodes;

barriers formed on the first dielectric layer in first and second directions; and

a second dielectric layer formed on the first substrate, wherein the first substrate electrode pairs have a predetermined height, [and] wherein the second dielectric layer includes a [groove of] plurality of grooves, each with a predetermined width and depth in the first and second directions on a surface region of the second dielectric layer, and wherein each groove is centrally located between two adjacent barriers.

15. (Twice Amended) The plasma display panel of claim 14, wherein the barriers in the first direction are perpendicular to the barriers in the second direction, and wherein the [groove forms an] grooves form exhaust [path in the second dielectric layer] paths.

16. (Twice Amended) The plasma display panel of claim 14, wherein [the groove is] grooves are formed in a region of the second dielectric layer [corresponding] approximately parallel to the barriers in the first direction.

17. (Twice Amended) The plasma display panel of claim 14, wherein the [groove is] grooves are formed in a region of the second dielectric layer [corresponding] approximately parallel to the barriers in the second direction.

18. (Twice Amended) The plasma display panel of claim 14, wherein the [groove is] grooves are formed in a region of the second dielectric layer corresponding to the barriers in the first direction and the barriers in the second direction.

19. (Twice Amended) The plasma display panel of claim 14, wherein the [groove is] grooves are formed in a corresponding region of the second dielectric layer only between the barriers in the first direction.

20. (Twice Amended) The plasma display panel of claim 14, wherein the [groove is] grooves are formed in a corresponding region of the second dielectric layer [between the barriers in the second direction] above the second substrate electrodes.

21. (Twice Amended) The plasma display panel of claim 14, wherein the [groove is]
grooves are formed in a corresponding region of the second dielectric layer between the barriers
in the first direction and between the barriers in the second direction.

22. (Twice Amended) The plasma display panel of claim 14, wherein the groove is
[wider] narrower than the barriers.

Clean Set of Amended Claims

14. (Twice Amended) A plasma display panel, comprising:

- a first substrate;
- a plurality of first substrate electrode pairs formed on the first substrate;
- a second substrate;
- second substrate electrodes formed on the second substrate to cross the first substrate electrode pairs;
- a first dielectric layer formed on the second substrate and the second substrate electrodes;
- barriers formed on the first dielectric layer in first and second directions; and
- a second dielectric layer formed on the first substrate, wherein the first substrate electrode pairs have a predetermined height, wherein the second dielectric layer includes a plurality of grooves, each with a predetermined width and depth in the first and second directions on a surface region of the second dielectric layer, and wherein each groove is centrally located between two adjacent barriers.

15. (Twice Amended) The plasma display panel of claim 14, wherein the barriers in the first direction are perpendicular to the barriers in the second direction, and wherein the grooves form exhaust paths.

16. (Twice Amended) The plasma display panel of claim 14, wherein grooves are formed in a region of the second dielectric layer approximately parallel to the barriers in the first direction.

17. (Twice Amended) The plasma display panel of claim 14, wherein grooves are formed in a region of the second dielectric layer approximately parallel to the barriers in the second direction.

18. (Twice Amended) The plasma display panel of claim 14, wherein the grooves are formed in a region of the second dielectric layer corresponding to the barriers in the first direction and the barriers in the second direction.

19. (Twice Amended) The plasma display panel of claim 14, wherein the grooves are formed in a corresponding region of the second dielectric layer only between the barriers in the first direction.

20. (Twice Amended) The plasma display panel of claim 14, wherein the grooves are formed in a corresponding region of the second dielectric layer above the second substrate electrodes.

21. (Twice Amended) The plasma display panel of claim 14, wherein the grooves are formed in a corresponding region of the second dielectric layer between the barriers in the first direction and between the barriers in the second direction.

22. (Twice Amended) The plasma display panel of claim 14, wherein the groove is narrower than the barriers.

C. Please add new claims 35-49 as follows:

35. (New) A plasma display device, comprising:

 a first substrate;

 a second substrate located in a spaced relation from the first substrate;

 a plurality of first substrate electrode pairs formed on the first substrate at constant intervals;

 barriers formed on the second substrate; and

 a first substrate dielectric layer formed on the first substrate and the plurality of first substrate electrode pairs having a plurality of grooves, wherein each first substrate electrode pair is located between two grooves.

36. (New) The plasma display panel of claim 35, wherein the plurality of grooves are each located to correspond with top portions of the barriers.

37. (New) The plasma display panel of claim 35, wherein each groove has a width wider than a width of a corresponding barrier.

38. (New) The plasma display panel of claim 35, wherein each groove has a predetermined depth which creates an exhaust path between the barriers.

39. (New) The plasma display panel of claim 35, wherein each groove is formed in a direction similar to a direction in which the barriers are formed.

40. (New) The plasma display panel of claim 35, wherein the barriers are formed in a lattice, wherein the barriers cross to form the lattice.

41. (New) The plasma display panel of claim 35, further comprising a second substrate dielectric layer formed between the second substrate and the barriers, and a plurality of address electrodes formed on the second substrate in a direction similar to a direction in which the barriers are formed.

42. (New) The plasma display panel of claim 35, wherein a lower portion of the first substrate dielectric layer including the grooves is in a spaced relation to upper portions of the barriers, and wherein grooves in the first substrate dielectric layer are only located above barriers.

43. (New) The plasma display panel of claim 35, wherein the grooves are only formed in a direction parallel to the first substrate electrode pairs.

44. (New) The plasma display panel of claim 14, wherein each groove is formed in a portion of the second dielectric layer corresponding to a location between the electrodes of each of the first substrate electrode pairs.

45. (New) A plasma display device, comprising:
a first substrate;
a second substrate located in a spaced relation from the first substrate;
a plurality of first substrate electrode pairs formed on the first substrate at constant intervals;
barriers formed on the second substrate; and
a first substrate dielectric layer formed on the first substrate and the plurality of first substrate electrode pairs having a plurality of grooves, wherein each groove is located between the pair of electrodes in each first substrate electrode pair.

46. (New) The plasma display panel of claim 45, wherein each groove is centrally located between barriers.

47. (New) A plasma display device, comprising:
a first substrate;
a second substrate located in a spaced relation from the first substrate;

a plurality of first substrate electrode pairs formed on the first substrate at constant intervals;

barriers formed on the second substrate; and

a first substrate dielectric layer formed on the first substrate and the plurality of first substrate electrode pairs having a plurality of grooves, wherein each groove is located above a barrier.

48. (New) The plasma display panel of claim 47, wherein each groove is formed in a direction approximately parallel to a direction in which the first substrate electrode pairs are formed.

49. (New) The plasma display panel of claim 48, wherein each groove is formed in a direction approximately perpendicular to a direction in which the first substrate electrode pairs are formed.